



United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER OF PATENTS AND TRADEMARKS Washington, D.C. 20231 www.hspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
	06/05/2001	William Jones	303.764US1	3744	
	an, lundberg, ŵ	EXAMINER			
P.O. BOX 2933 MINNEAPOL	8 IS, MN 55402	6	LE, THONG QUOC		
	2	J	ART UNIT	PAPER NUMBER	
			2818		
			DATE MAILED: 01/15/2003		

Please find below and/or attached an Office communication concerning this application or proceeding.

<u> </u>		Application No.	Applicant(s)	—— j
❤` •.		09/874,894	JONES ET AL.	•
	Offic Action Summary	Examiner	Art Unit	
	•	Thong Q. L	2818	
	The MAILING DATE of this communication	appears on the cov r sh et	with the correspondence address -) =
Peri d f		IN VIOLET TO EVOIDE A	MONTHO FROM	
THE N - Exter after - If the - If NO - Failur - Any re earne	ORTENED STATUTORY PERIOD FOR REMAILING DATE OF THIS COMMUNICATIOn signs of time may be available under the provisions of 37 CFF SIX (6) MONTHS from the mailing date of this communication period for reply specified above is less than thirty (30) days, a period for reply is specified above, the maximum statutory perestore to reply within the set or extended period for reply will, by steply received by the Office later than three months after the mid patent term adjustment. See 37 CFR 1.704(b).	N. R. 1.136(a). In no event, however, may reply within the statutory minimum of nod will apply and will expire SIX (6) N atute. cause the application to become	a reply be timely filed thirty (30) days will be considered timely. IONTHS from the mailing date of this communical ABANDONED (35 U.S.C. § 133).	ation.
Status	Responsive to communication(s) filed on			
1)□	•	This action is non-final.		
2a)□	Since this application is in condition for all	,	natters, prosecution as to the meri	its is
3)	closed in accordance with the practice und	der <i>Ex parte Quayle</i> , 1935	C.D. 11, 453 O.G. 213.	
-	on of Claims			
-	Claim(s) 1-29 and 42-53 is/are pending in			
	4a) Of the above claim(s) is/are with	drawn from consideration.		
- '=	Claim(s) is/are allowed.			
•	Claim(s) <u>1-29 and 42-53</u> is/are rejected.			
•	Claim(s) is/are objected to.			
• —	Claim(s) are subject to restriction ar on Papers	na/or election requirement.		
	The specification is objected to by the Exam	niner		
<i>,</i> —	The drawing(s) filed on is/are: a)☐ a		y the Examiner.	
10)	Applicant may not request that any objection t			
11)[The proposed drawing correction filed on _			
,—	If approved, corrected drawings are required in			
12) 🔲	The oath or declaration is objected to by the	e Examiner.		
Priority (ınder 35 U.S.C. §§ 119 and 120			
13)	Acknowledgment is made of a claim for for	reign priority under 35 U.S.	C. § 119(a)-(d) or (f).	
a)	☐ All b)☐ Some * c)☐ None of:			
	1. Certified copies of the priority docum	nents have been received.		
	2. Certified copies of the priority docum	nents have been received i	n Application No	
* 0	3. Copies of the certified copies of the application from the Internationa See the attached detailed Office action for a	il Bureau (PCT Rule 17.2(a)).	
	Acknowledgment is made of a claim for dom			cation).
-) ☐ The translation of the foreign language			,
15) <u> </u>	Acknowledgment is made of a claim for don	nestic priority under 35 U.S	.C. §§ 120 and/or 121.	
Attachmen				
2) Notic	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948 mation Disclosure Statement(s) (PTO-1449) Paper No) 5) Notice	ew Summary (PTO-413) Paper No(s) of Informal Patent Application (PTO-152)	_·
J.S. Patent and T	rademark Office			

Application/Control Number: 09/874,894

Art Unit: 2818

DETAILED ACTION

1. Amendment filed on November 08, 2002 has been entered.

2. Claims 1-29, 42-53 are presented for examination.

Information Disclosure Statement

- This office acknowledges receipt of the following items from the Applicant:
 Information Disclosure Statement (IDS) filed on November 08, 2002.
- 4. Information disclosed and list on PTO 1449 was considered.

Claim Rejections - 35 USC § 102

5. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in-
- (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effect under this subsection of a national application published under section 122(b) only if the international application designating the United States was published under Article 21(2)(a) of such treaty in the English language; or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that a patent shall not be deemed filed in the United States for the purposes of this subsection based on the filing of an international application filed under the treaty defined in section 351(a).
- 6. Claims 1-29, 42-53 are rejected under 35 U.S.C. 102(e) as being anticipated by OOISHI et al. (U.S. Patent No. 2002/0051404).

Regarding claims 1-53, OOISHI et al. disclose a memory device (Figure 1, 1000) comprising a delay locked loop (Figure 5, 300) for generating an internal clock signal (ABSTRACT) based on an external clock signal (Figure 5, CLK), the DLL keeping the external and internal clock signal synchronized by performing a synchronization

Application/Control Number: 09/874,894

Art Unit: 2818

operation (ABSTRACT); and a DLL controller (figure 4, 330) having a selector (Figure 5, SW) connected to the DLL for selectively activating a DLL control signal during a test mode of the memory device to prevent the DLL from performing the synchronization operation during the test mode (ABSTRACT, Column 3, paragraph 0039 and 0054) as claims 1, 8 disclose, and a plurality of memory cells (104) as claim 2 disclose, and during test mode the memory cells are activated in preparation for subsequent access to the memory cells (ABSTRACT) as claims 3-5, 9-11 disclose, and the DLL includes a phase detector (Figure 4, 320) and a shift register (Figure 200) as claims 6, 12, 15, 17 disclose, and DLL controller includes a test mode select input (Figure 1, TM from 46), and output to the DLL (Figure 4) as claims 7, 13, 18 disclose, and the memory device including a plurality of inputs for receiving a plurality of input signals and an external clock signal (Figure 1, 2, 10, 30, 70) as claim 14 discloses and the DLL includes a delay lines have a plurality of delay stages connected in series (Figure 4, Figure 11) as claim 16 discloses, and an output connected to the phase detector providing the DLL control signal (Figure 4, UP, DOWN) as claims 19-20 disclose, and system comprising a processor (paragraph 0004) as claims 21-29 disclose, and the memory device includes an input for receiving an external clock signal (Figure 1, 2, ABSTRACT) as claims 42-53 disclose.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thong Q. Le whose telephone number is 703-306-9123. The examiner can normally be reached on 8:00am-5:00pm M-F.

Application/Control Number: 09/874,894

Art Unit: 2818

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David C. Nelms can be reached on 703-308-4910. The fax phone numbers for the organization where this application or proceeding is assigned are 703-872-9318 for regular communications and 703-872-9319 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-306-3329.

Thong Q. Le Examiner

Art Unit 2818

January 9, 2003